MAY 2 4 2004 Reference: ESI-116-B

AP 2882

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Patrick F. Leonard

Serial No.

09/680,342

Filing Date:

October 5, 2000

Examiner/Art Unit

C. Kao/2882

Title:

METHOD AND APPARATUS FOR EVALUATING

INTEGRATED CIRCUIT PACKAGES HAVING THREE

DIMENSIONAL FEATURES

CERTIFICATE OF MAILING AND TRANSMITTAL LETTER

Commissioner for Patents P O Box 1450 Alexandria VA 22313-1450

Sir:

Transmitted with this document is a Postcard, Supplemental Information Disclosure Statement, Form SBO8A & SBO8B and European Search Report along with Cited References, in the above-identified application.

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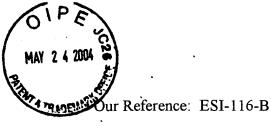
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Attorney for Applicant Registration No. 37,089

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. § 1.97

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The references listed in the attached form SBO8A & SBO8B are cited pursuant to Rule 37 C.F.R. 1.56 to meet the duty to disclose to the Patent Office all information known to the inventor, attorney or any other person who is substantively involved in the preparation or prosecution of the application or who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.

If applicable, copies of the International Search Report and any foreign patent office Search Report are enclosed for the Examiner's use, and these reports indicate the relevance of the cited references according to the respective search authorities.

These references constitute all the information of which the individuals pursuant to 37 C.F.R. § 1.56(c) are currently aware.

Respectfully submitted,

YOUNG & BASILE, P.C.

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Dated: May 21, 2004 TEB/iml

PTO/SB/08A (08-03)

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Sheet

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known		
Application Number	09/680,342	
Filing Date	October 5, 2000	
First Named Inventor	Patrick F. Leonard	
Art Unit	2882	
Examiner Name	C. Kao	
Attorney Docket Number	FSI-116-R	

			U. S. PATENT D	OCUMENTS	
Examiner nitials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
		Number-Kind Code ^{2 (if known)}			Figures Appear
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	FOREIGN PATENT DOCUMENTS						
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Substitution form 1449/PTO		Complete if Known			
PANE	7101 701111 144071 10			Application Numb r	09/680,342
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STATEMENT BY APPLICANT		First Named Inventor	Patrictk F. Leonard		
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Sheet		of		Attorney Docket Number	ESI-166-B

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		MENGEL P: "Automated inspection of solder joints on PC board by supplementary processing of 3D and gray-level images" Signal Processing and SYStem Control,	
		factory Automation. Pacific Grover, Nov 27-30 1990, Proceedings of the annual conference of the Industrial Electronics Society. (IECON), New York IEEE, US Vol. 1 Conf. 16.	
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